

FIGURE 1

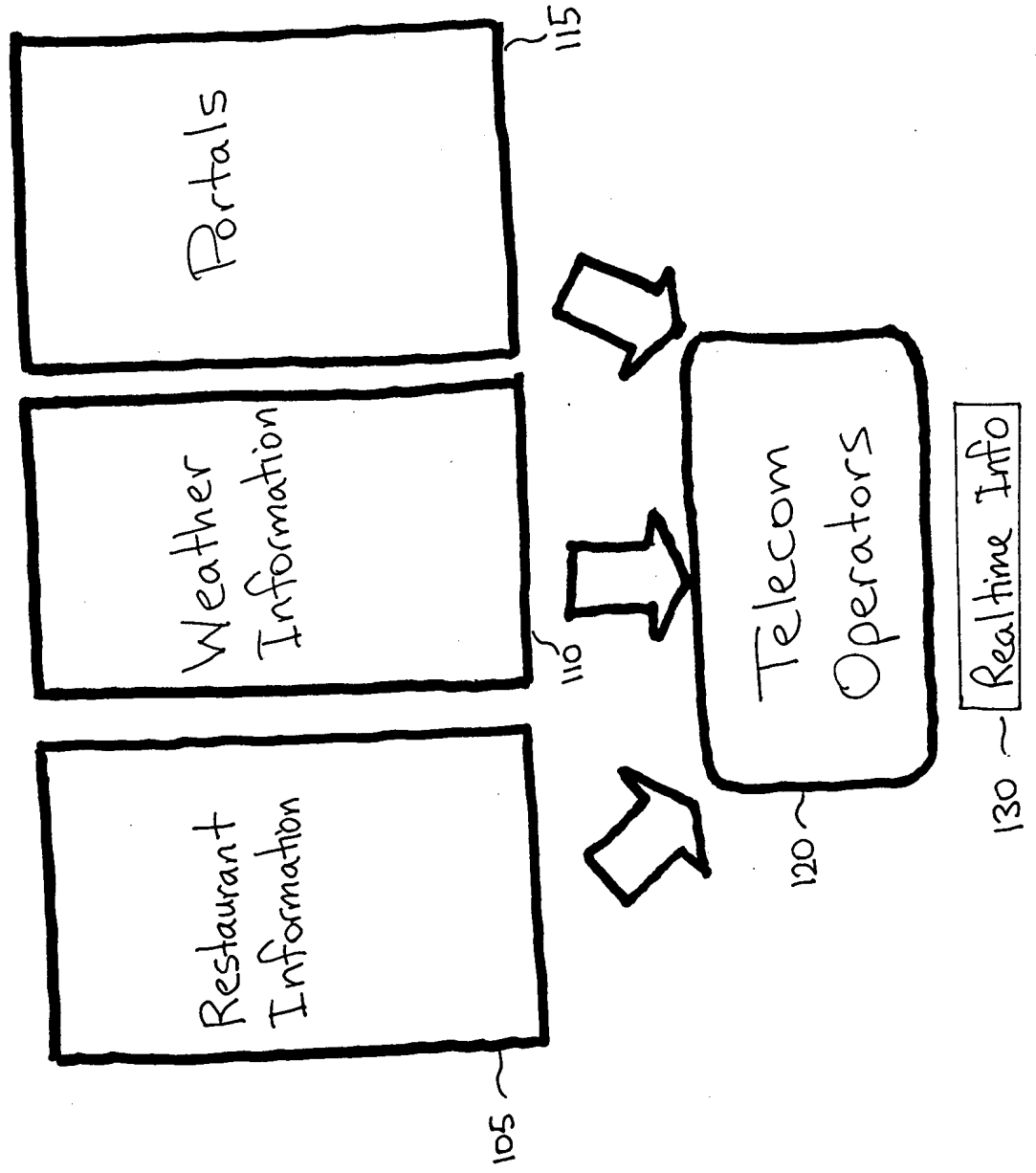
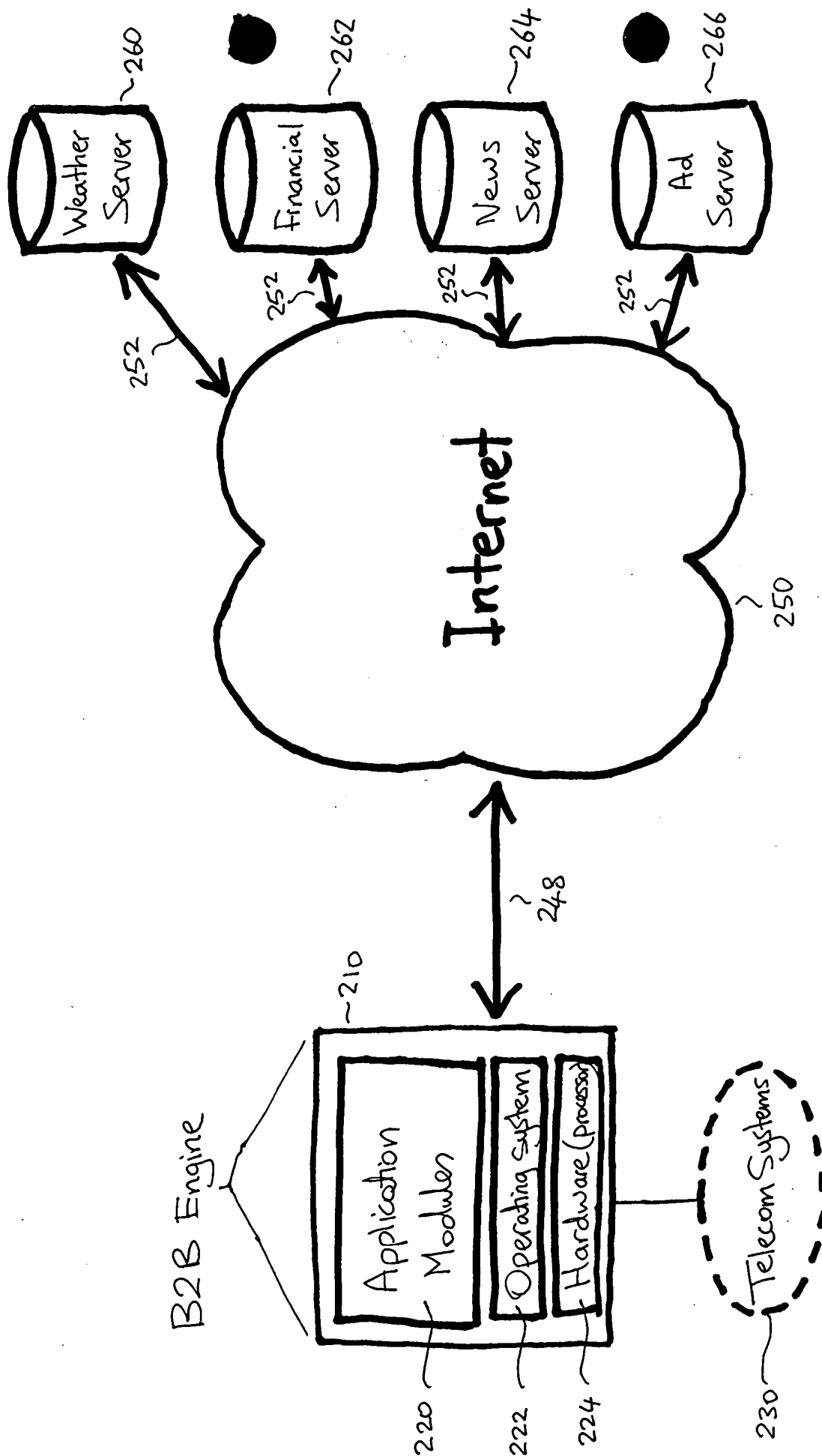


FIGURE 2



# FIGURE 3

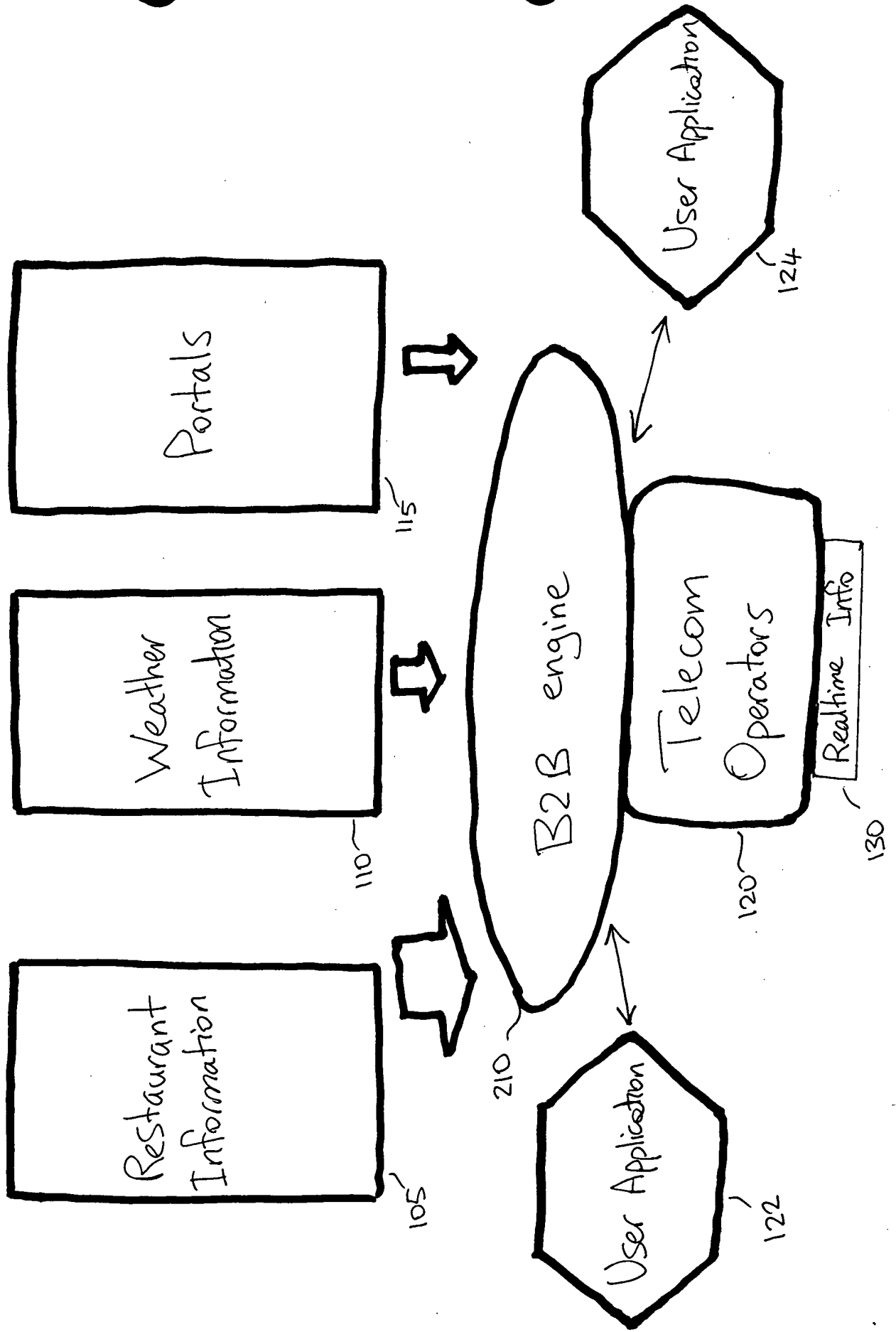
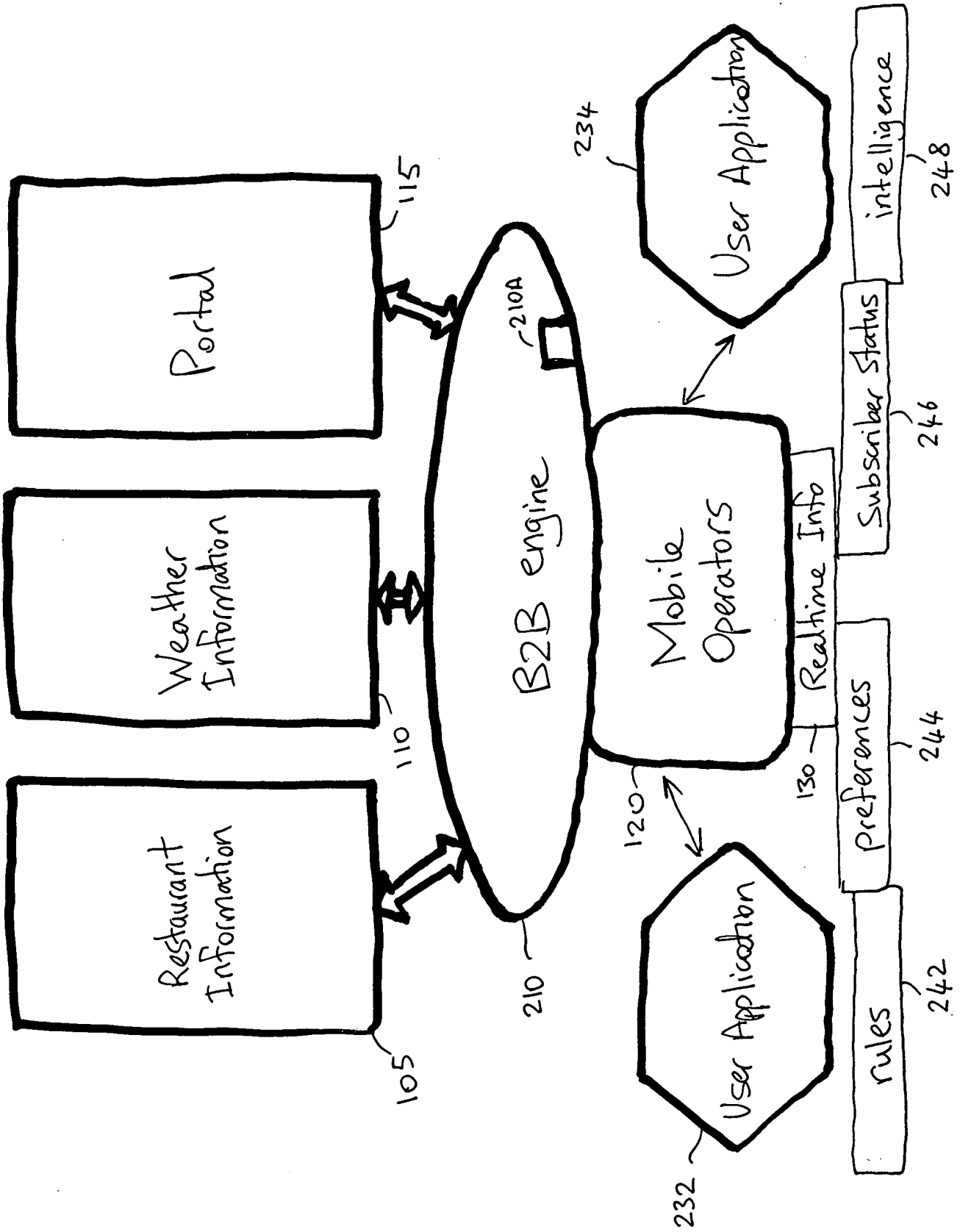


FIGURE 4



# FIGURE 5

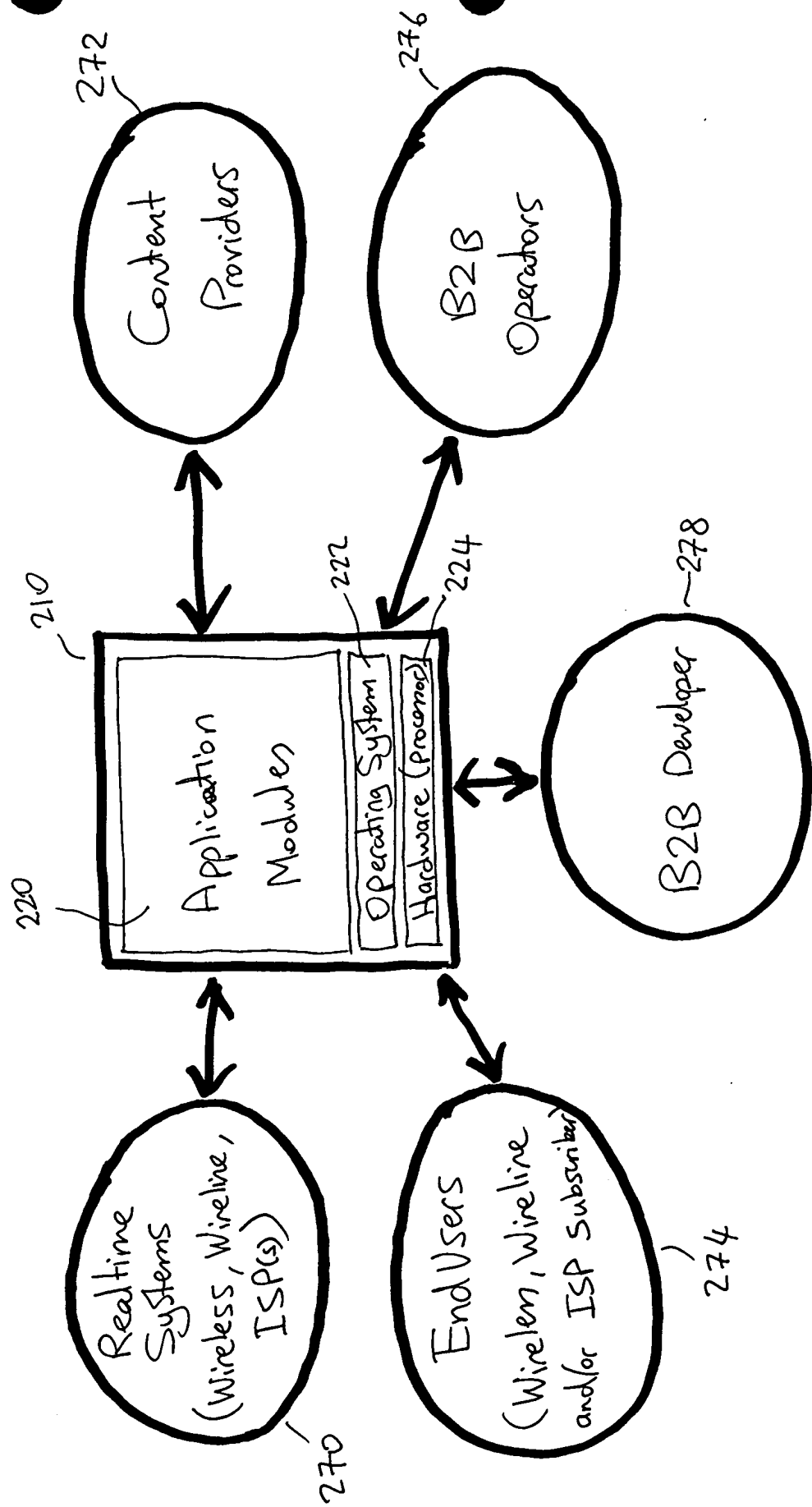
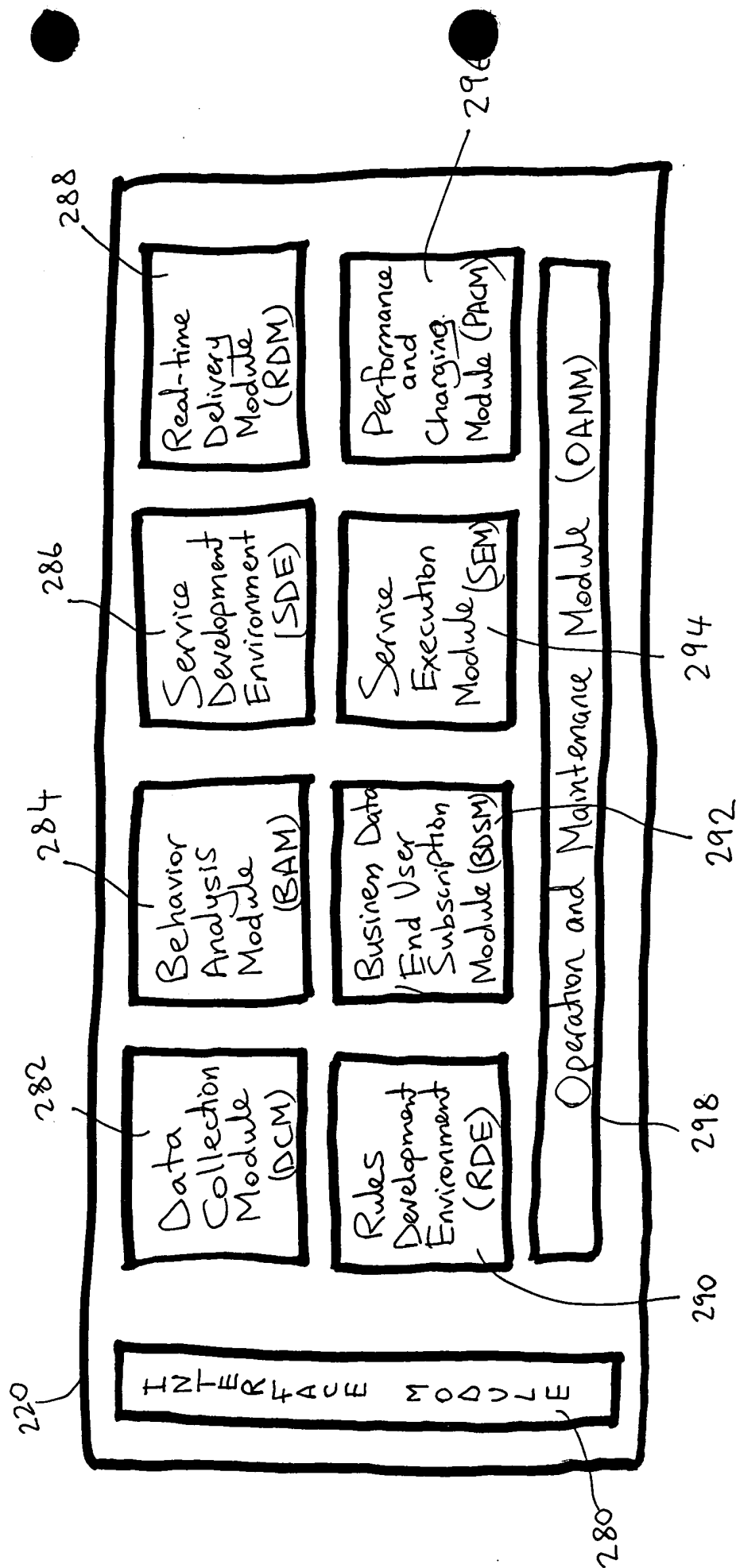
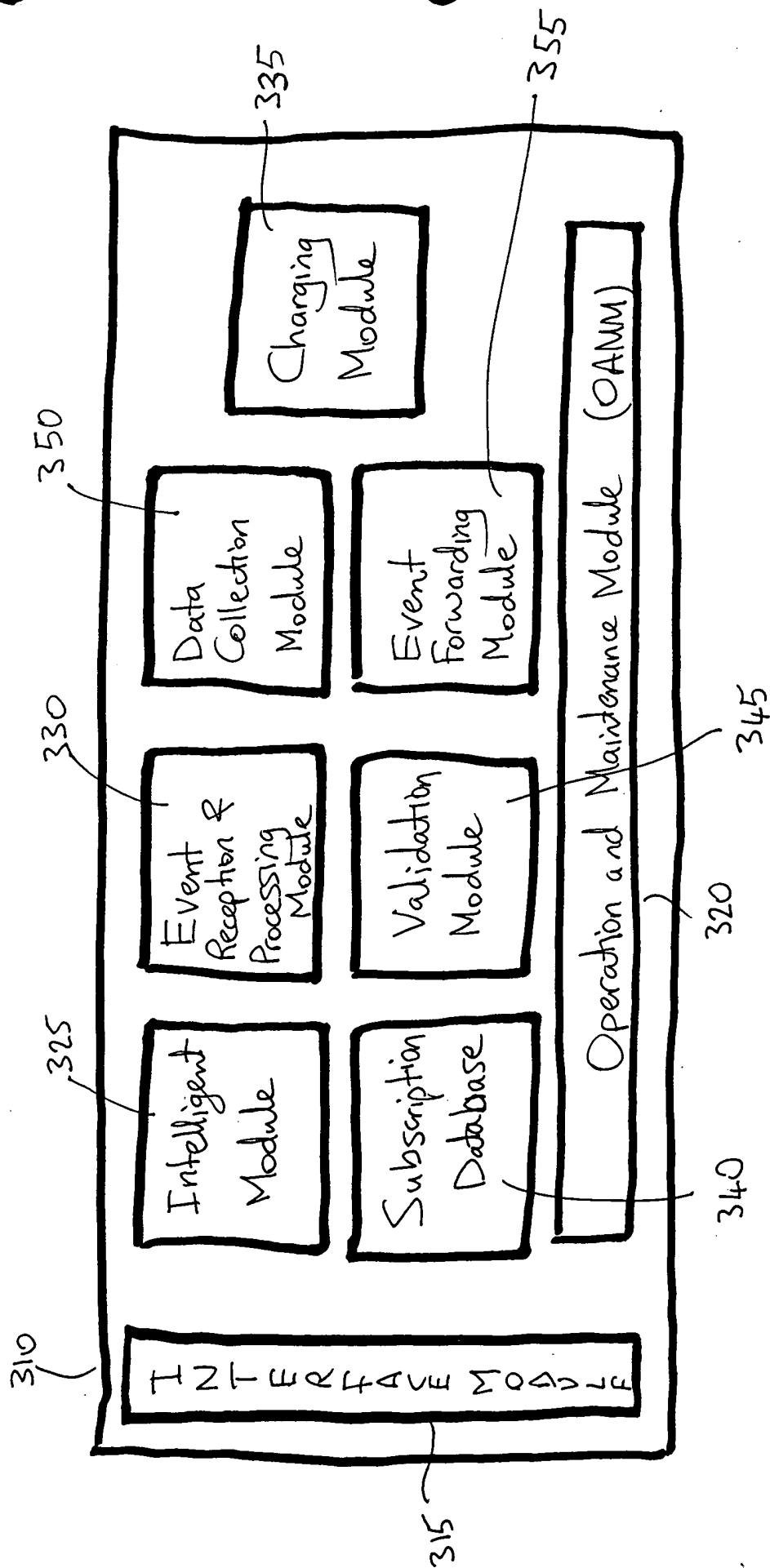


FIG. 6 is a block diagram of a system architecture.

# FIGURE 6



# FIGURE 7



# FIGURE 8

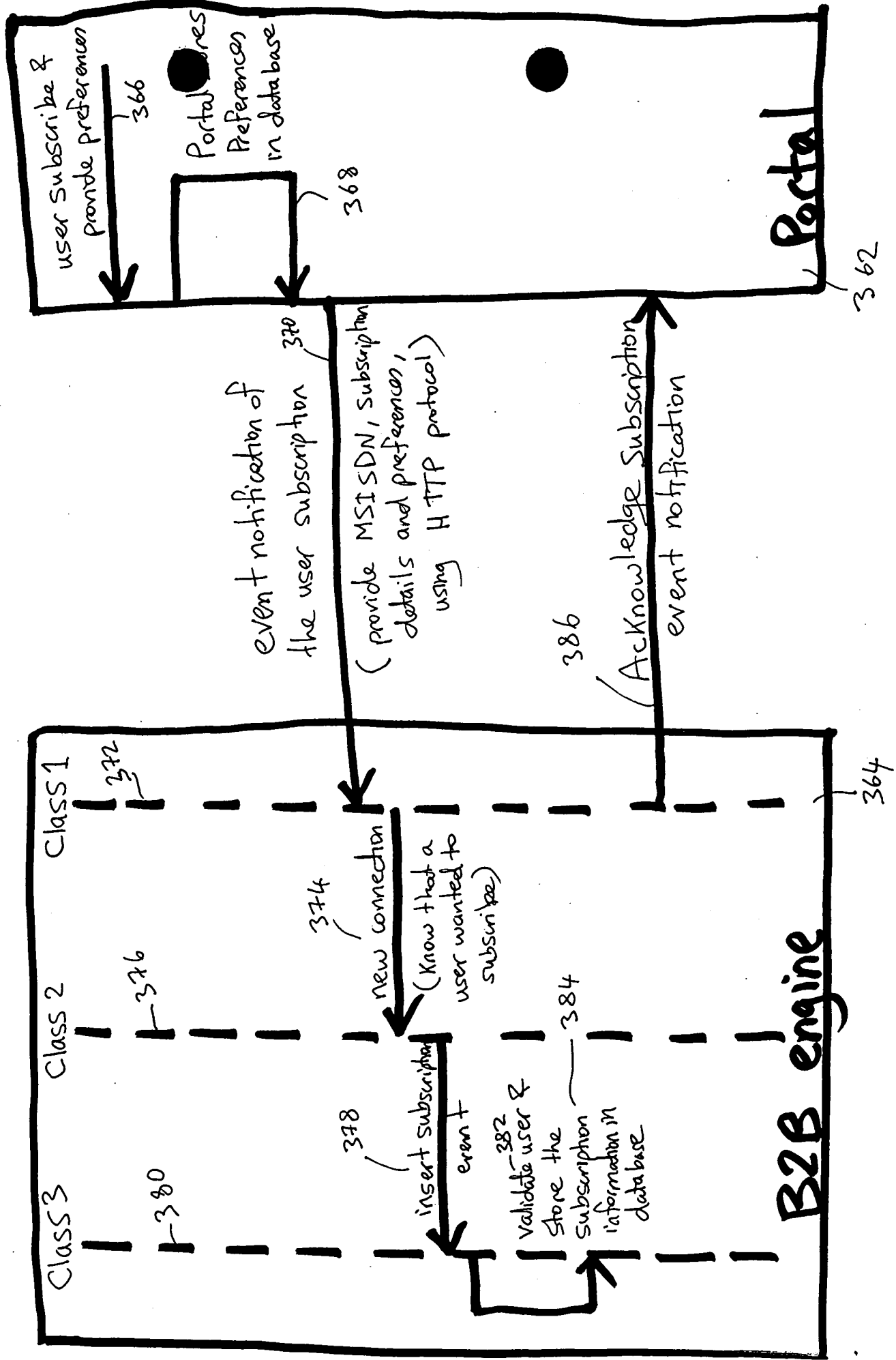




FIGURE 9

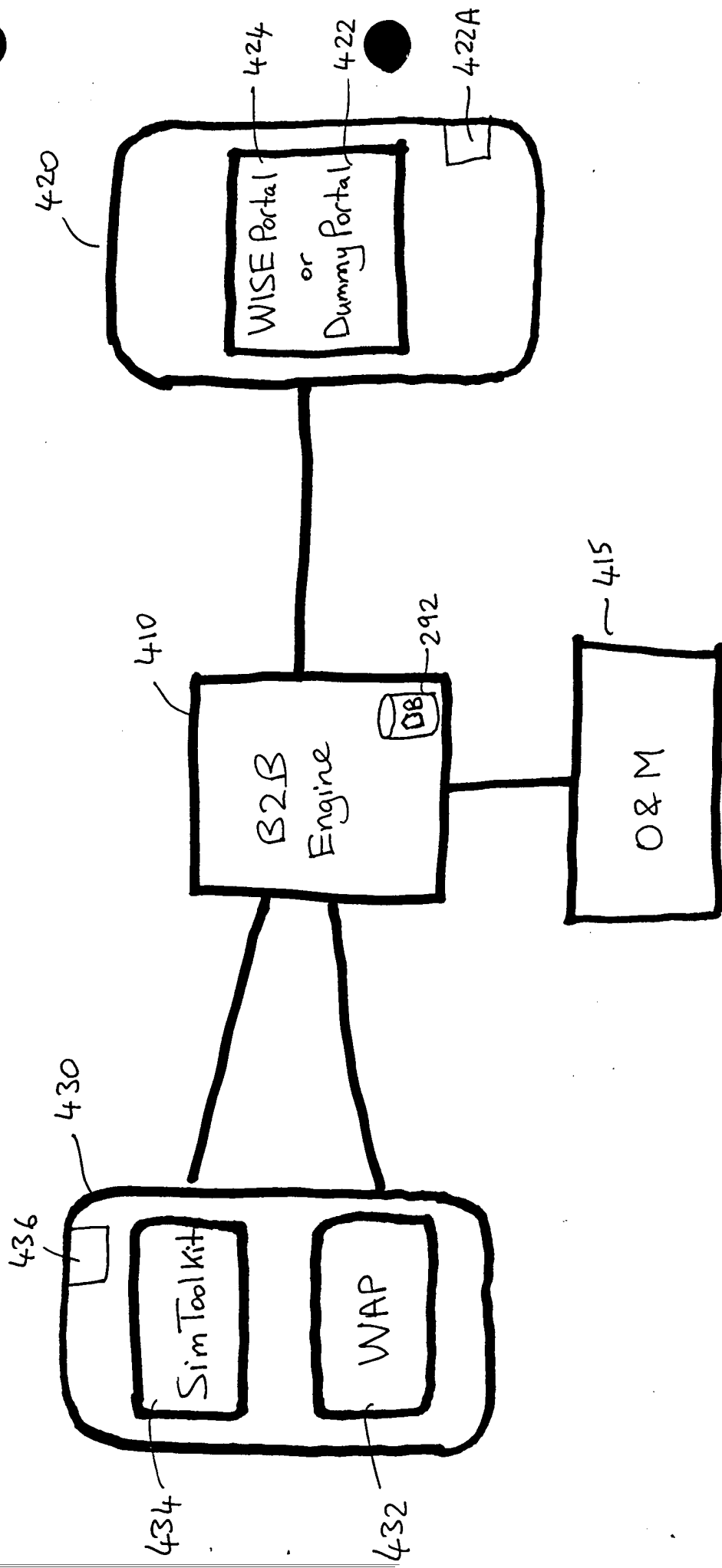


FIGURE 10

450

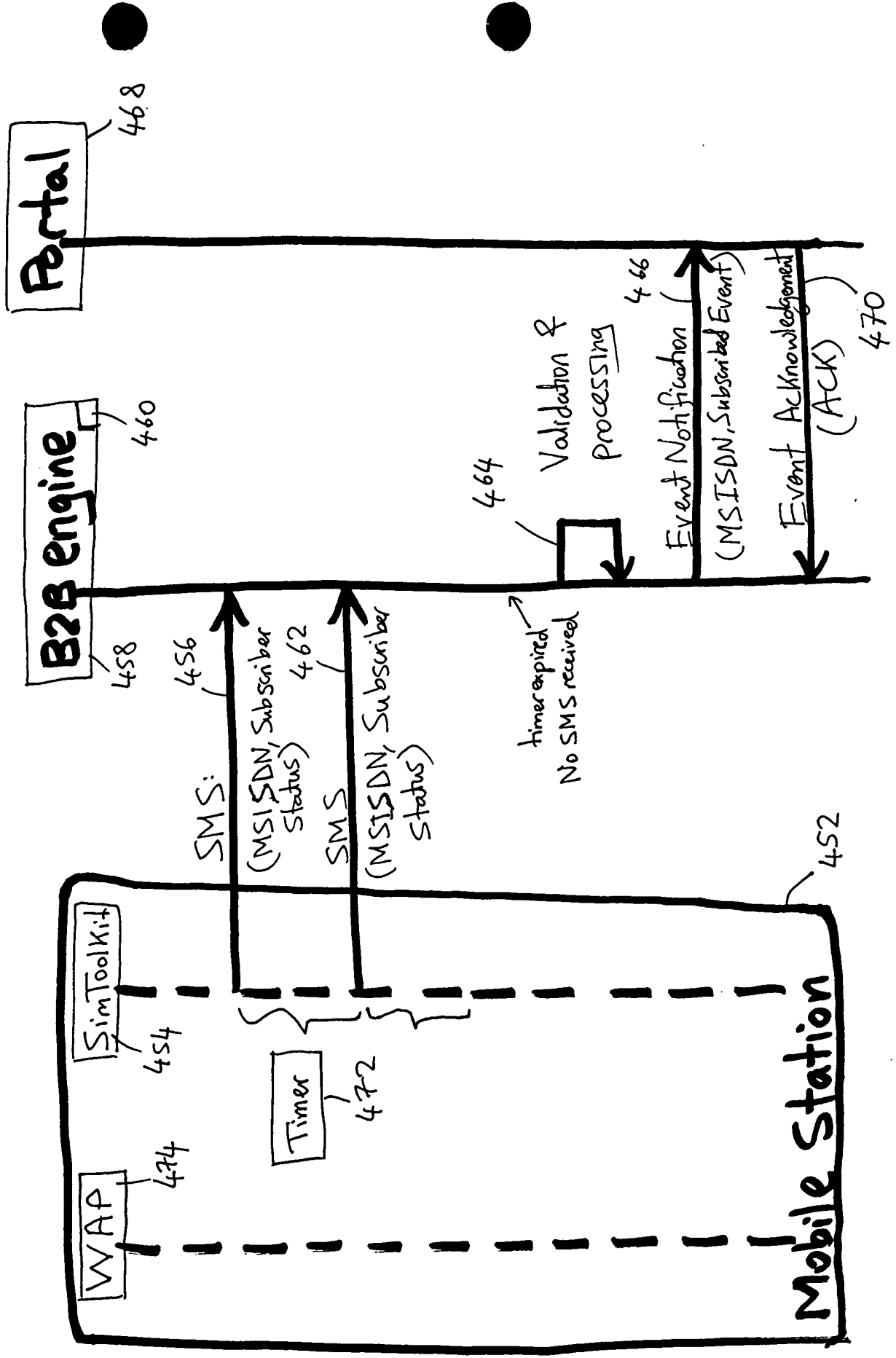
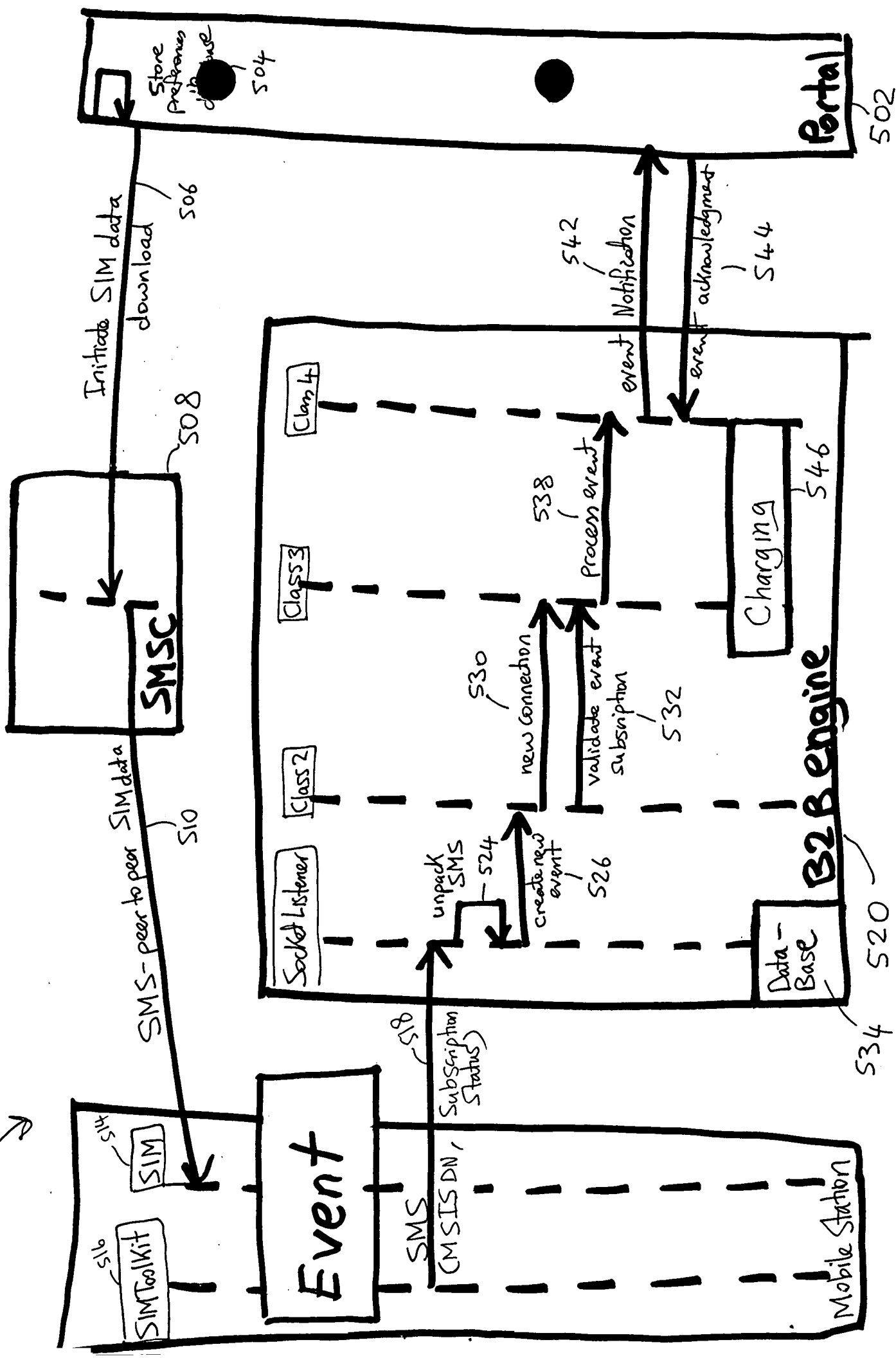


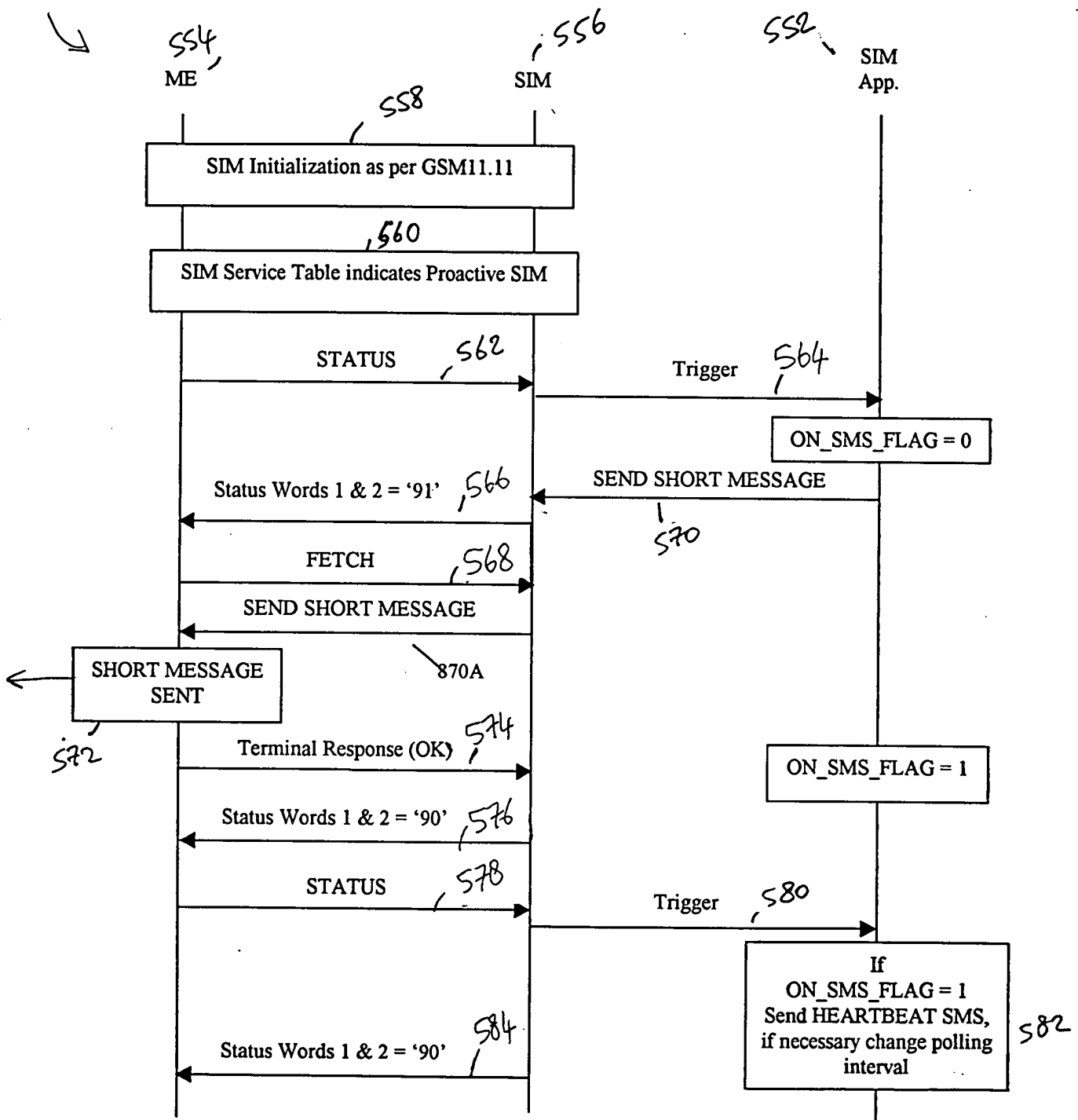
FIGURE 11

500



# FIGURE 12

550



# FIGURE 13

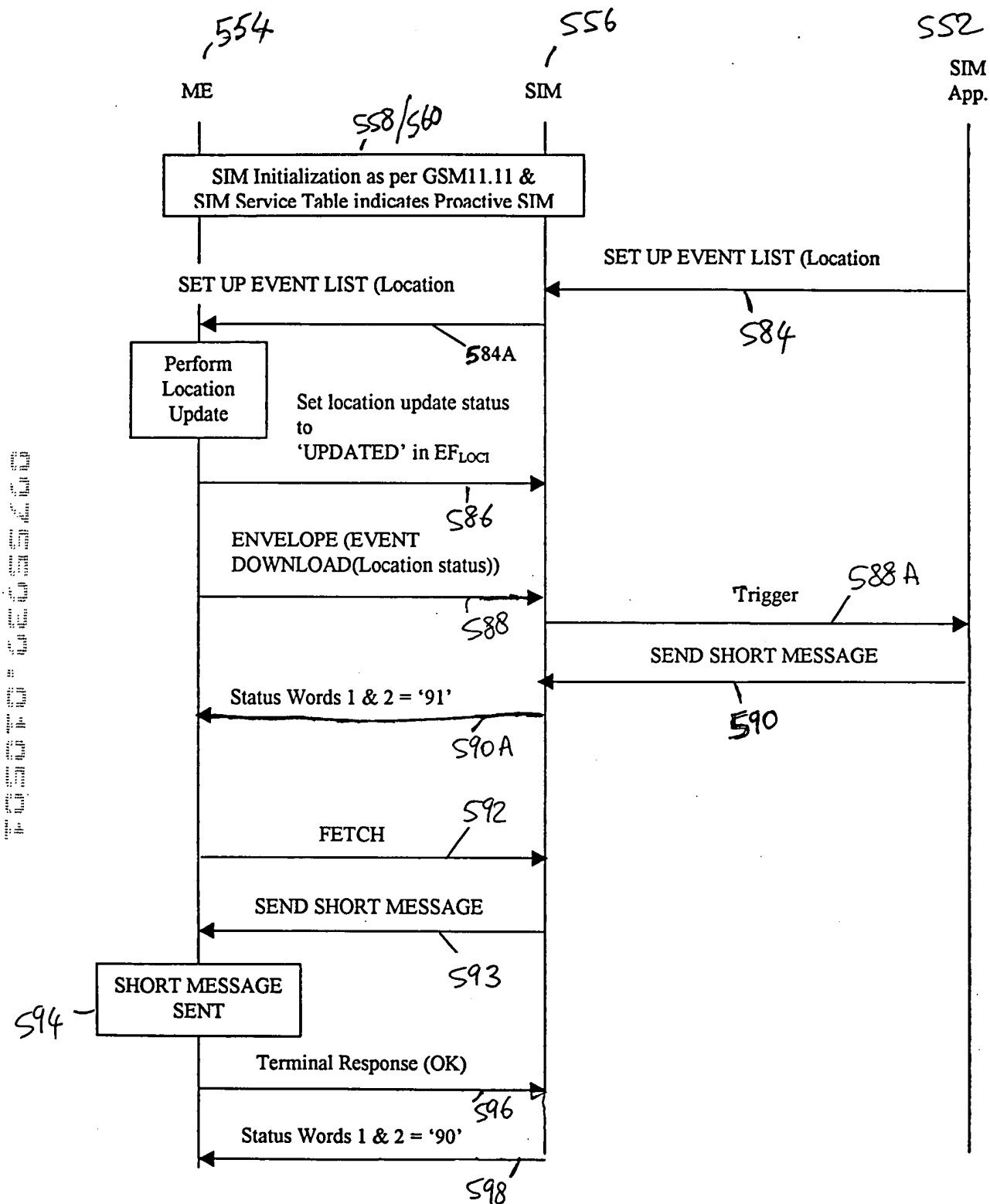


FIGURE 14

600

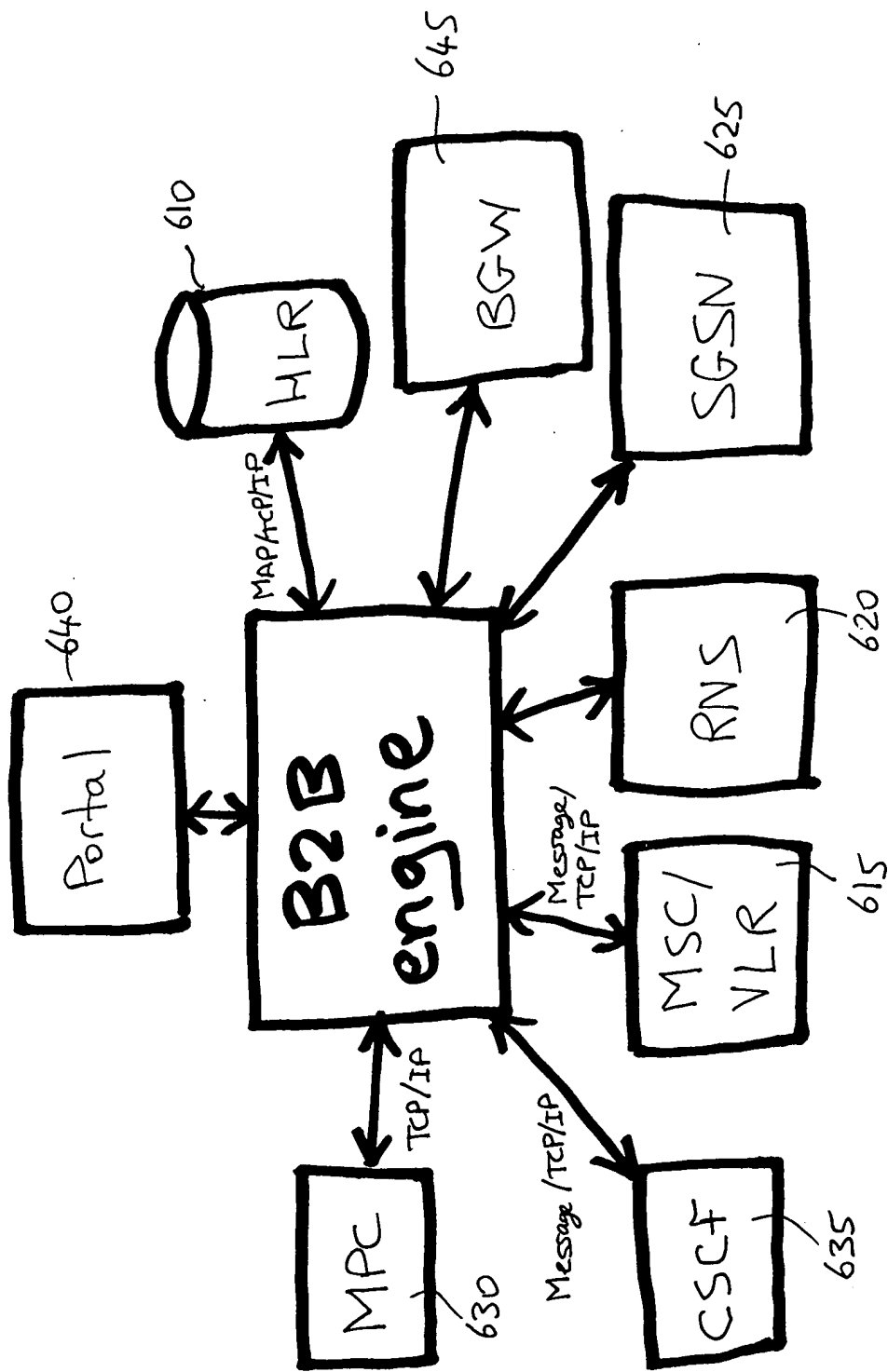
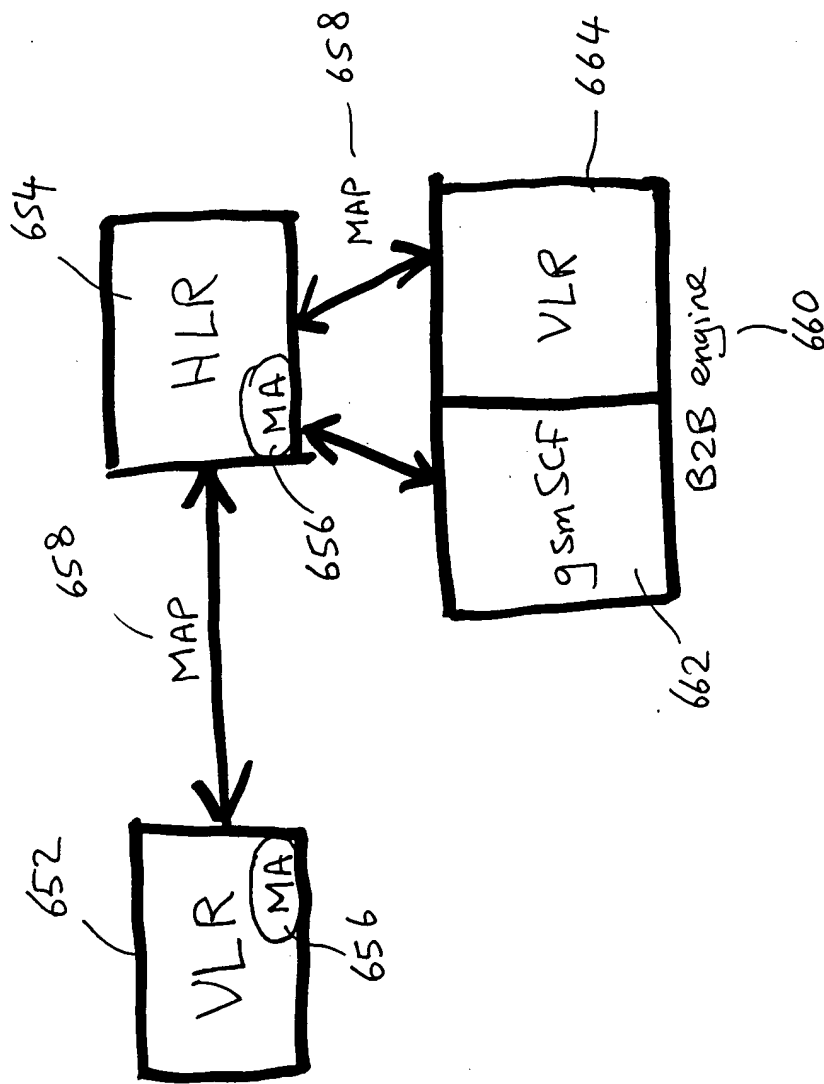
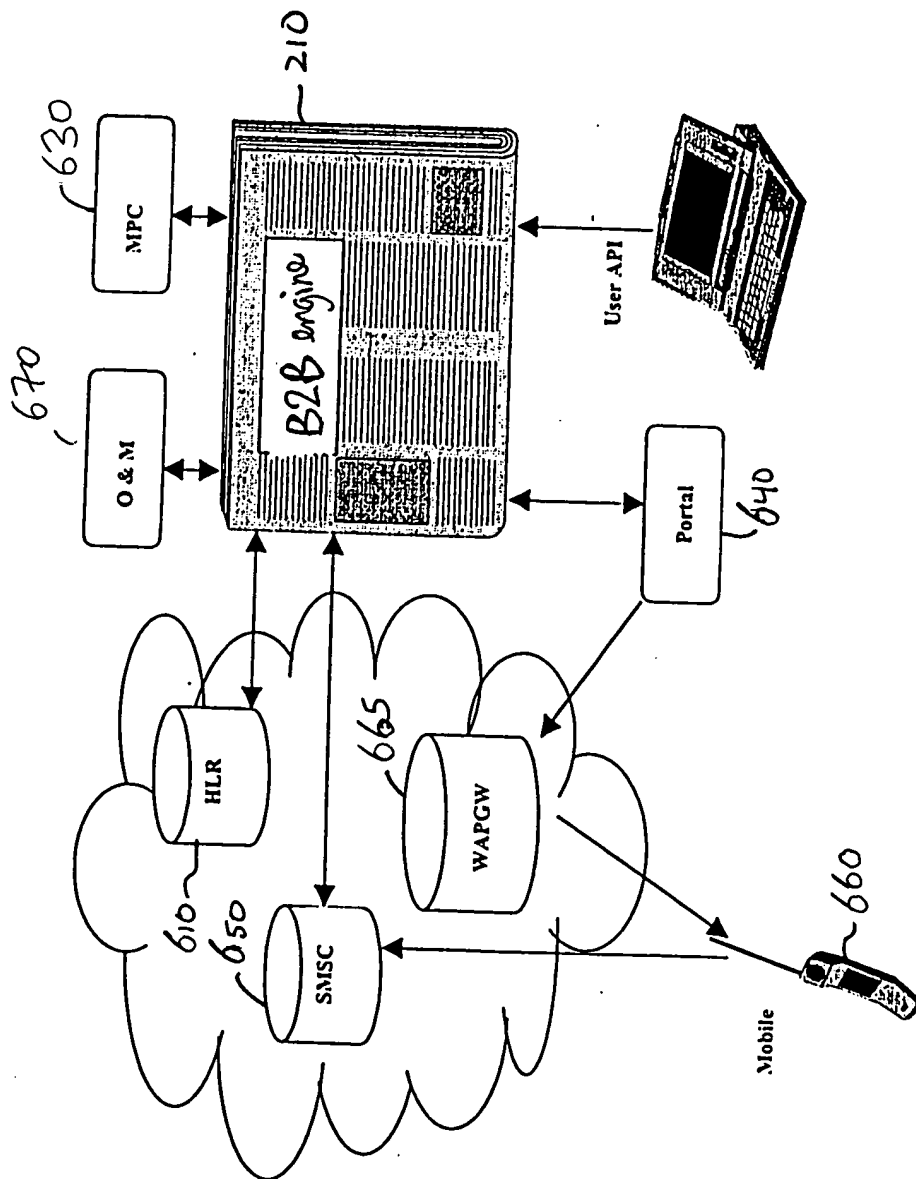


FIGURE 15

650

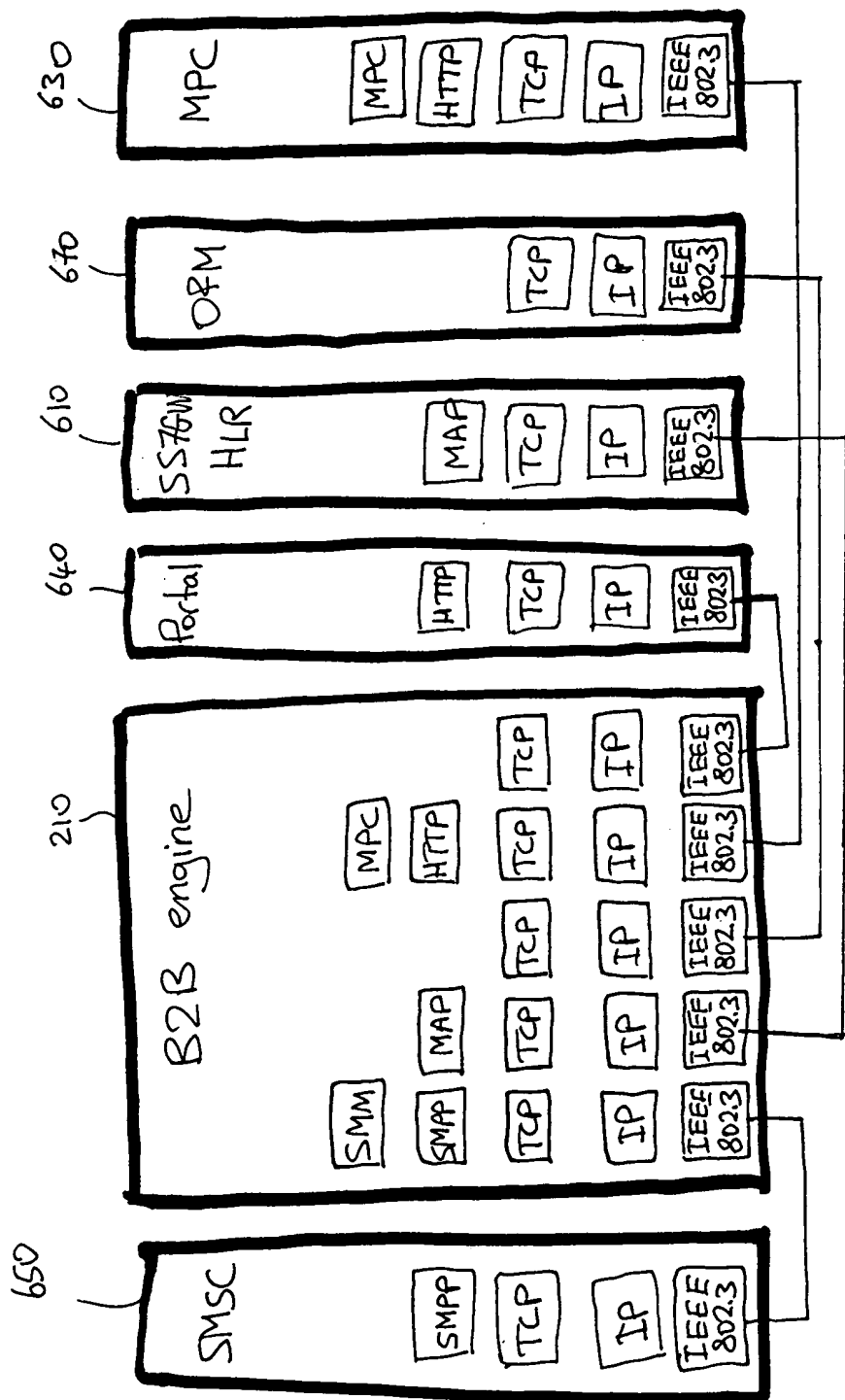


# FIGURE 16





# FIGURE 17



# FIGURE 18

